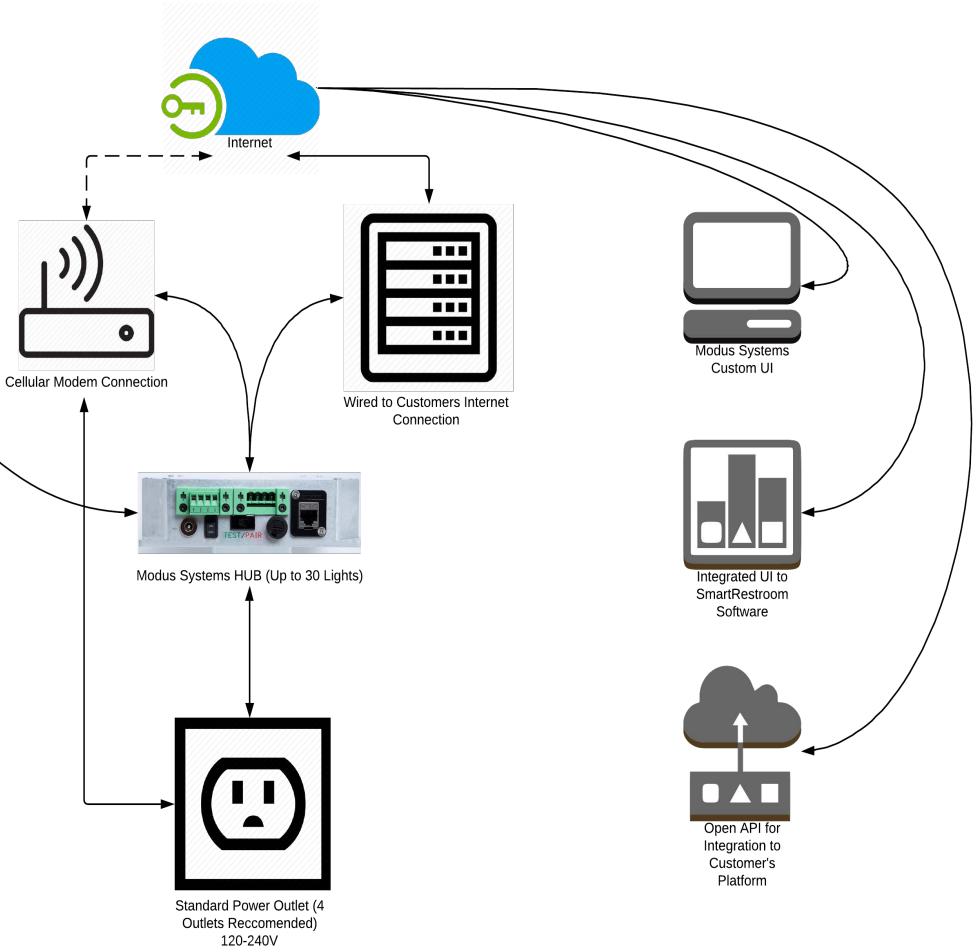
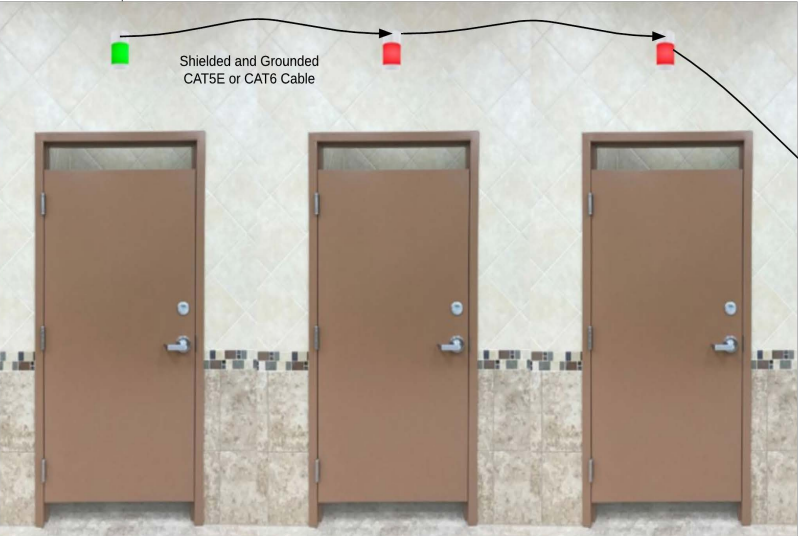
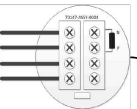


# System Diagram

Connection with Resistor (Termination)



# System Diagram

